



IN THE US PATENT AND TRADEMARK OFFICE

Application Number: 10/023,910  
Agent's Docket Number: CBL-104/DIV  
5 Filing Date: December 18, 2001  
Applicant: Glenn S. Solomon et al.  
Application Title: DUAL PROCESS SEMICONDUCTOR  
HETEROSTRUCTURES  
Examiner: Victor V. Yevsikov  
10 Art Unit: 2825

<u>Certificate of Mailing by "EXPRESS MAIL"</u>	
I hereby certify that I am mailing this correspondence on the date indicated below to the MAIL STOP NON-FEE AMENDMENT, COMMISSIONER FOR PATENTS, P.O. Box 1450, Alexandria, VA 22313-1450 using the "Express Mail Post Office to Addressee" service of the United States Postal Service under 37 CFR 1.10.	
<u>Bing Li</u>	DATE OF MAILING 1/2/2004
SIGNATURE	EXPRESS MAIL LABEL NO: EF081084385US
<u>Bing Li</u>	
NAME OF PERSON SIGNING	

AMENDMENT C

15 Commissioner of Patents and Trademarks  
Washington, DC 20231

Sir:

In response to the Office Action mailed October 8, 2003 (paper no. 6), kindly amend the application as follows and consider the following remarks:

20 **AMENDMENTS TO THE CLAIMS** are reflected in the **LISTING OF ALL CLAIMS** that begins on page 2 of this paper.

**REMARKS** begin on page 4 of this amendment.

1

1

RECEIVED  
JAN - 8 2004  
TECHNOLOGY CENTER 2800